

# ***PinEditor Standalone v6.1 User's Guide***



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## Welcome to PinEditor Standalone

PinEditor is a graphical application for assigning I/O ports to package pins.

**Note:** PinEditor Standalone supports the ACT1, ACT2, ACT3, MX, DX, eX, SX, and SX-A families. If you are designing for the ProASIC3E, ProASIC3, ProASIC <sup>PLUS</sup>, Axcelerator, and ProASIC families, use PinEditor in MultiView Navigator. See the MultiView Navigator User's Guide for more information.

Use PinEditor to:

- Assign I/O macros to pins
- Lock pin assignments that have been automatically assigned during layout
- View and print pin assignments
- Assign I/O standards to banks (for families that use I/O banks)
- Edit I/O attributes
- Assign VREF pins (for I/O standards that require an input reference voltage)

## Starting PinEditor

To start PinEditor from Designer, either click the **PinEditor** icon in the Designer Design Flow window, or from the **Tools** menu, choose **PinEditor**. PinEditor displays the pins and I/O macros in your design.

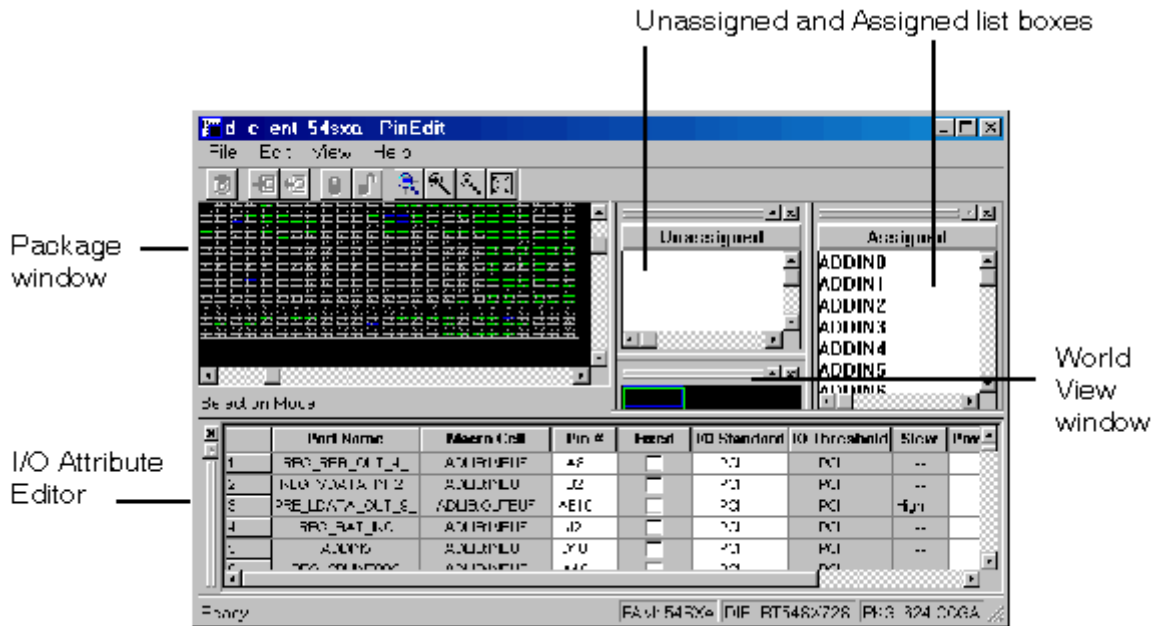
## Components of PinEditor

The PinEditor interface is divided into five windows:

- Package window - view your design elements
- Assigned list box - displays macros currently assigned to pins
- Unassigned list box - displays unassigned macros
- World view window - select the area of the design to display in the Package window
- I/O Attribute Editor - view, sort, select, and edit I/O attributes

These windows are highly integrated; anything selected in one is selected or highlighted in all. Commands are accessible from the menu bar and frequently-used commands are on the toolbar.

All the windows and toolbars are independently sizeable, dockable, and closable. To redock or refloat a window, double-click the window's title bar.



PinEditor Standalone

**Note:** On UNIX systems, floating windows are not sizeable once undocked, and the I/O Editor does not float.

## Package Window

PinEditor's Package window displays a graphical interpretation of the pin locations for your selected package type.

The **Package window** is integrated with PinEditor windows and list boxes. If you select an assigned pin in the Package window, the pin location is highlighted in the **World View** window and the I/O macro name is selected in the **Assigned** list box and the **I/O Attribute Editor**.

The Package Window displays detailed information about each pin, including:

- Pin number
- Special pin properties, such as JTAG, clock, ground, or power
- Assigned I/O macro name, if any
- Pin type, represented by color

## Colors and Symbols

Colors and symbols differentiate the pin and logic I/O macro assignments in PinEditor. The following table indicates the default colors assigned to pins.

Color/Symbol	Definition
White border	A white border denotes a selected, assigned pin.
Green	A green border with a black center denotes a regular, unassigned pin. A pin with a grey or yellow border and a green center denotes a regular, assigned pin.
Blue	A blue border with a black center denotes a special, unassigned pin. A pin with a grey or yellow border and a blue center denotes a special, assigned pin. Special pins are pins that have some additional meaning to them. For example, pins used for JTAG are blue. When unassigned, special pins have additional descriptive text next to the pin number.
Grey with red center	Reserved pin. You use this pin for some specific purpose on the package, and you cannot assign it an I/O macro. Examples of such pins are ground and power.
Yellow	Yellow denotes <i>locked</i> assignments. If the assignment is selected, the symbol appears yellow. If the assignment is unselected, the border appears yellow.
Grey/black	A pin with a grey border and a black center denotes a pin that is not connected. You cannot use these pins, and they have no meaning.

## Assigned and Unassigned List Boxes

The Assigned and Unassigned list boxes display assigned or unassigned I/O macros in the design. By default, all assigned I/O macros are displayed in the Assigned list box and all unassigned I/O macros are displayed in the Unassigned list box.

You can filter which pins are displayed in these list boxes. To customize the Assign and Unassign list boxes, from the **View** menu, choose **Configure List Boxes**.

## I/O Attribute Editor Window

The I/O Attribute Editor window appears below the Package and World View windows. It functions much like a spreadsheet with sort, copy, and paste capabilities. This editor allows selection of variables such as I/O Standard, I/O Threshold, slew rate, and I/O Power-Up State, depending on the family targeted. You can also enter the capacitance of the load being driven. You can lock assigned pins for subsequent place-and-route runs allowing parallel development of the printed circuit board (PCB).

Use the I/O Attribute Editor to view, sort, select, and edit these attributes. Double-click a column heading to sort by that attribute. If you select a macro in the Assigned or Unassigned list box, it will also appear selected in the I/O attribute editor.

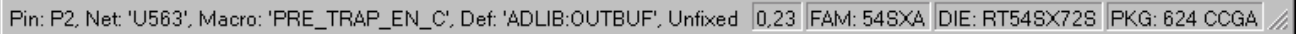
## World View Window

The World View window's default location is under the Assigned list box. Use the World View window to control which portion of the package is displayed in the Package window. The blue rectangle (known as the Package rectangle) represents the package. The green rectangle (known as the Viewing rectangle) represents the currently displayed area in the Package window.

To display another part of the package, use the left mouse button to drag the **Viewing rectangle** to the area on the **Package rectangle** you would like to display. To specify a new display area, use the right mouse button to drag out a new **Viewing rectangle** on the **Package rectangle**.

## Status Bar

Family, die, and package information appears in the right corner of the status bar. Additionally, the status bar displays information on commands, pins, and error messages.



Pin: P2, Net: 'U563', Macro: 'PRE\_TRAP\_EN\_C', Def: 'ADLIB:OUTBUF', Unfixed 0,23, FAM: 54SXA, DIE: RT54SX72S, PKG: 624 CCGA

### *To see other information in the status bar:*

- Hold your mouse over a pin in the **Package** window to see the pin number, instance name, macro cell, and locked or unlocked status.
- Hold your mouse over a toolbar button or a menu command to see a short description of that command.
- Error messages provide details about invalid assignment attempts. Choose **Extended Error Message** from the **Help** menu for more information.

## Making Pin Assignments

Use PinEditor to make and edit I/O macro pin assignments. Edits you make in PinEditor are permanent, provided they are locked and have been committed.

### *To assign an I/O macro to a pin:*

1. Select the macro name in the **Unassigned** list box. The macro is simultaneously selected in the **I/O Attribute Editor**.
2. Assign the selected macro to a pin location using any one of these methods:
  - Drag the selected macro name from the **Unassigned** list box to the pin location in the **Package** window. Valid pin locations are highlighted in the Package Window.
  - From the **Edit** menu, choose **Assign** to invoke the Assign mode. Then, select the pin location in the **Package** window.
  - Click the **Assign** toolbar button to invoke the Assign mode, and then select the pin location in the Package window.
  - If you know the specific pin location, enter the pin assignment in the **Pin#** cell or select a valid placement from the drop-down menu.

If the location is a valid one, the macro is assigned and automatically locked. The status bar displays information about invalid assignments. Choose **Extended Error Messages** from the **Help** menu for more information about specific error messages.

**Note:** If you assign a macro to a pin that has already been assigned a macro, the previously assigned macro becomes unassigned if it was not locked.



## Unassigning Pins

Unassign an I/O macro from a locked assignment when its locked position is no longer required. Unassigned macros are automatically placed in optimum locations during layout.

### *To unassign a macro from a pin:*

1. Select the I/O macro in the **Assigned** list box.
2. Unassign the selected macro from a pin location using any one of these methods:
  - Drag the selected macro name from the **Assigned** list box to the **Unassigned** list box.
  - From the **Edit** menu, choose **Unassign**.
  - Click the **Unassign** toolbar button.
  - Press the **DELETE** key.
  - In the **I/O Attribute Editor**, select **Unassign** in the **Pin #** cell.

The macro is unassigned.

## Locking and Unlocking Pins

Designer does not alter locked pins during Layout. Designer recognizes pins as locked when they are assigned in one of the following ways:

- Manually using PinEditor in a design schematic
- Using a pin file (all Antifuse families except for Axcelerator)
- Using a PDC file (ProASIC3E, ProASIC3, and Axcelerator families only)
- Using a GCF file (Flash families only)

Locked pins are permanent, provided that you commit your locked pins to your design before you exit PinEditor. To save changes to disk (in your .adb file), use the **Save** command in Designer before exiting PinEditor.

### *To lock pins:*

1. Select the pin(s) to lock in the **Assigned** list box, **Package** window, or **I/O Attribute Editor**. To select multiple pins, hold down the **CTRL** key and select multiple pins. To select all pins, from the **Edit** menu, choose **Select All**.
2. From the **Edit** menu, choose **Lock**, or select the **Locked** check box in the **I/O Attribute Editor**.

**Note:** When you use the I/O Attribute Editor, you can only lock one pin at a time.

### *To unlock a pin:*

1. Select the pin(s) to unlock in the **Assigned** list box, Package window, or **I/O Attribute Editor**. To select multiple pins, hold down the CTRL key and select multiple pins. To select all pins, from the **Edit** menu, choose **Select All**.
2. From the **Edit** menu, choose **Unlock**, or clear the **Locked** check box in the **I/O Attribute Editor**.

## Closing and Committing Pin Assignments

The changes you make to your pin assignments and I/O attributes in PinEditor are temporary until you commit them.

- To commit your pin assignments at any time, from the **File** menu, choose **Commit**.
- To commit your pin assignments when closing PinEditor, click **Yes** when prompted.

Committing your changes saves them to the “working” design for this Designer session only.

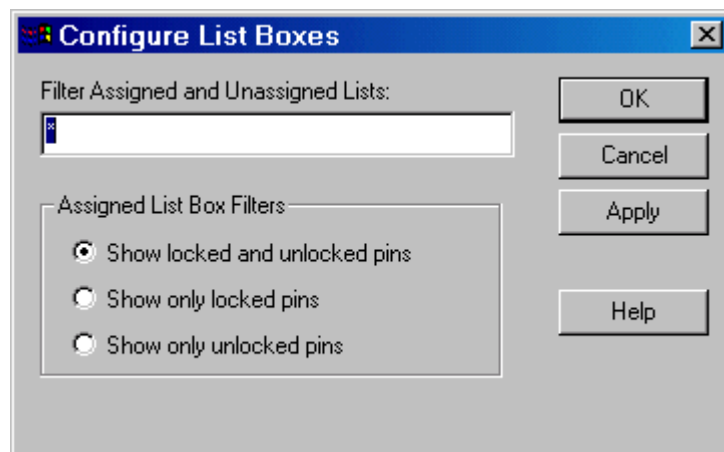
To save changes made in PinEditor to disk, you must save your design by choosing **Save** from the **File** menu in Designer.

## Customizing Assigned and Unassigned List Boxes

You can customize which pins are displayed in the **Assigned** and **Unassigned** list boxes.

*To customize the list boxes:*

1. From the View menu, choose Configure List Boxes.
  - **Filter Assigned and Unassigned Lists** - Entering a specific pin name in this field filters out all other pins in the Assigned and Unassigned list boxes. Use the \* wildcard to filter for groups.
  - **Show locked and unlocked pins** - Select to display all locked and unlocked pins in the Assigned list box
  - **Show only locked pins** - Select to filter out all unlocked pins from the Assigned list box
  - **Show only unlocked pins** - Select to filter out all locked pins from the Assigned list box
2. Click **Apply** to see changes. When done, click **OK**.



Configure List Boxes Dialog Box

## Scripting Commands

You can make pin assignments, lock and unlock pins, commit pin assignments, and edit I/O attributes by running Tool Command Language (Tcl) scripts. You can run scripts from the Windows or UNIX command line or store and run a series of commands in a .tcl batch file.

For more information on PinEditor Tcl extension commands, see “Scripting” in the Designer User’s Guide.

## Assigning I/O Macros

The Pin cell in the **I/O Attribute Editor** displays either the I/O macro's assigned Pin number or its Unassigned status. If you know the pin number assignment, type the location into the Pin cell.

Edits you make in PinEditor are permanent, as long as they are locked and committed.

*To assign an I/O macro using the I/O Attribute Editor:*

1. Select the **Pin** cell for the desired macro in the **I/O Attribute Editor**.
2. In the **Pin** cell, type the pin number or select a valid pin assignment from the drop-down list.

**Note:** If you assign a macro to a pin that has already been assigned a macro, the previously assigned macro becomes unassigned. Since PinEditor locks all assigned macros, the reassignment unlocks the previous assignment.

## Editing Multiple Rows

*To edit multiple rows:*

1. Select the rows to edit. To select consecutive rows, click the first row, press and hold down the **SHIFT** key, and then click the last row. To select rows that are not consecutive, press and hold down the **CTRL** key, and then click each row to select. Continue to hold down the **SHIFT** or **CTRL** key.
2. While still holding down the **SHIFT** or **CTRL** key, click in the cell containing the value you want to change. Release the **SHIFT** or **CTRL** key, and then release the mouse button. The change occurs in all selected rows.

**Note:** You can also select an entire column, which enables you to edit all rows.

## Sorting I/O Attributes

*To sort I/O macros by attributes:*

- Double-click a column heading to sort the table rows in ascending order
- Double-click the same column again to sort the table rows in descending order

## Setting the Slew

**Note:** The Slew attribute supports only SX-A and RTSX-S families.

The default slew displayed in the I/O Attribute Editor is based on the I/O Standard.

The Slew cell indicates the slew rate for output buffers. Generally, available slew rates are high and low. For those devices that support additional slew values, Actel recommends that you use the high and low values and let the software map to the appropriate absolute slew value.

*To set slew:*

1. Select **CUSTOM** (SX-A and RTSX-S) from the **I/O Standard** cell in the macro row to edit.
2. Enter the slew or select the slew rate from the **Slew** list box.

**Note:** The slow slew rate is incompatible with 3.3V PCI requirements.

## Specifying Capacitance

**Note:** The Capacitance attribute supports only SX-A and RTSX-S families.

The default output capacitance appears in the Loading (pf) column of the I/O Attribute Editor. This default value is based upon the I/O specification set in the I/O Standard cell. If necessary, you can change the output capacitance default setting to improve timing definition and analysis. Both the capacitive loading on the board and the  $V_{il}/V_{ih}$  trip points of driven devices affect output-propagation delay.

The I/O Attribute Editor provides a mechanism for setting the expected capacitance to improve the propagation-delay model. Timer, Timing-Driven Layout, Timing Report, and Back-Annotation will automatically use the modified delay model for delay calculations.

**To specify capacitance:**

1. Select **CUSTOM** from the **I/O Standard** cell in the desired macro row.
2. Set capacitance in the **Loading** (pf) column. You can modify the capacitance value to any integer value that accurately reflects the capacitive loading on the Actel device pins. Increasing capacitance will increase propagation delays.

## Specifying I/O Threshold

**Note:** The I/O Threshold attribute supports only SX-A and RTSX-S families.

The default I/O Threshold displayed is based upon the I/O Standard. If you want to set the I/O Threshold independently of the I/O specification, you must select CUSTOM in the I/O Standard cell.

**To specify I/O Threshold:**

1. Select **CUSTOM** from the **I/O Standard** cell in the macro row you want to edit.
2. Select **CMOS**, **TTL**, or **PCI** from the **I/O Threshold** cell. CMOS should be faster on high-to-low transitions, and TTL should be faster on low-to-high transitions.

## Common I/O attributes (All Families)

The I/O Attribute Editor displays four common attributes for all I/O macros:

- **Port Name** indicates the I/O macro name.
- **Macro Cell** indicates the type of I/O macro.
- **Pin #** indicates the current pin assignment.
- **Locked**, if checked, indicates that you cannot change the current pin assignment during layout.

Besides the common I/O attributes, the I/O Attribute Editor displays device-specific attributes. Only attributes applicable to a specific device appear in the I/O Attribute Editor table.

Common Attributes					Device-Specific Attributes					
	Port Name	Macro Cell	Pin #	Locked	Bank Name	I/O Standard	Output Drive (mA)	Slew	Resistor Pull	Inp
1	M_DIOV_STOP	ADLIB:TRIBUFF	N4	<input type="checkbox"/>	Bank6	LVTTTL	24	High	None	
2	T_IDE_A(1)	ADLIB:OUTBUF	J22	<input type="checkbox"/>	Bank2	LVTTTL	24	High	None	
3	CRC32_SRAMA(2)	ADLIB:OUTBUF	P3	<input type="checkbox"/>	Bank6	LVTTTL	24	High	None	
4	IDEDATA(10)	ADLIB:BIBUF	U14	<input type="checkbox"/>	Bank4	LVTTTL	24	High	None	
5	CRC32_SRAMA(14)	ADLIB:OUTBUF	T7	<input type="checkbox"/>	Bank5	LVTTTL	24	High	None	
6	CRC32_SRAMA(20)	ADLIB:OUTBUF	M40	<input type="checkbox"/>	Bank6	LVTTTL	24	High	None	

## SX-A and RTSX-S I/O Attributes

**Note:** For SX-A/RTSX-S families, you must use the I/O Attribute Editor inside of PinEditor Standalone to edit I/O attributes.

Use the I/O Attribute Editor to set or modify the following attributes for SX-A and RTSX-S devices:

- I/O Standard - Refer to the appropriate datasheet for more information about I/O standards for different families
- I/O Threshold
- Slew
- Resistor Pull
- Hot Swappable
- Loading (pf)


The following table provides a summary of all supported I/O features for SX-A and RTSX-S devices:

I/O Standard	Device			Output Slew-rate Control	Power-Up State Control	Hot-Swap	Loading (pf)
	SX-A	eX	RT54SX-S				
2.5V LVC MOS	X	X		X	X	Yes	35
3.3V LV TTL	X	X	X	X	X	Yes	35
5V TTL	X	X	X	X	X	Yes	35
3.3V PCI	X		X	High	X	No	10
5V PCI	X		X	High	X	Yes	50
5V CMOS			X	X	X	Yes	35





## Menus, Toolbar Buttons, and Shortcut Keys

The PC and workstation (UNIX) versions of PinEditor have the same menus. However, some dialog boxes may look slightly different due to the different windowing environments. The functionality is the same, though the locations of the fields and buttons on the dialog boxes may vary. Field names may also vary between platforms.





### File menu

Command	Icon	Shortcut	Function
Commit			Saves your changes to the working design for this Designer session only <b>Note: To save changes to disk, you must also save your file in Designer.</b>
Prelayout Check			Ensures that the design can be placed and routed
Print		CTRL + P	Prints the design displayed in the Package window
Close			Closes PinEditor

### Edit menu

Command	Icon	Shortcut	Function
Copy		CTRL + C	Copies the selection to the Clipboard
Assign		CTRL + I	Assigns the selected I/O macro to the next selected pin
Unassign		DEL key	Unassigns the selected I/O macro
Lock		CTRL + F	Locks the selected I/O macro
Unlock			Unlocks the selected I/O macro
Select All		CTRL + A	Selects all assigned I/O macros in your design
Configure I/O Banks			Not used

## View menu

Command	Icon	Shortcut	Function
Zoom Area			Drag out an area to enlarge
Zoom In		+	Magnifies the view by a factor of 2 (scale = 2x)
Zoom Out		-	Reduces the view by a factor of 2 (scale = .5x)
Fit in Window		CTRL + W	Fits the entire design within the Package window
Redraw		F2	Redraws the design in the Package window
Configure List Boxes...			Displays the Configure List Boxes dialog box, in which you can filter which pins appear in the Assigned and Unassigned list boxes
Color Manager			Displays the Color Manager dialog box, which does nothing in PinEditor but changes the colors of nets, clusters, and super clusters in ChipEditor
I/O Banks			Not used
Toolbar			Hides or displays the toolbar buttons
Assigned List Box			Hides or displays the Assigned List Box
Unassigned List Box			Hides or displays the Unassigned List Box
World View			Hides or displays the World View window
I/O Attribute Editor			Hides or displays the I/O Attribute Editor at the bottom of the window
Status bar			Hides or displays the status bar

## Help menu

Command	Shortcut	Function
Help Topics		Displays online Help for this version of PinEditor
Reference Manual		Opens the PinEditor User's Guide (.pdf file) in Adobe Acrobat
Extended Error Message		Displays a dialog box with more detailed information



## Contacting Actel

### Actel Headquarters

Actel Corporation is a supplier of innovative programmable logic solutions, including field-programmable gate arrays (FPGAs) based on Antifuse and Flash technologies, high-performance intellectual property (IP) cores, software development tools, and design services targeted for the high-speed communications, application-specific integrated circuit (ASIC) replacement, and radiation-tolerant markets.

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### Technical Support

Highly skilled engineers staff the Technical Support Center from 7:00 A.M. to 6:00 P.M. Pacific Time, Monday through Friday.

#### Visit Tech Support Online

For 24-hour support resources, visit Actel Technical Support at <http://www.actel.com/custsup/search.html>.

#### Contacting Technical Support

Contact us with your technical questions via e-mail or by phone. When sending your request to us, please be sure to include your full name, company name, email address and telephone number.

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